## DATA SHEET



# MOS FIELD EFFECT TRANSISTOR **3SK131**

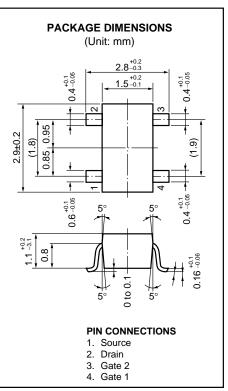
### RF AMP. FOR VHF TV TUNER N-CHANNEL SILICON DUAL-GATE MOS FIELD-EFFECT TRANSISTOR 4PIN MINI MOLD

#### FEATURES

- Suitable for use as RF amplifier in VHF TV tuner.
- Low Crss : 0.05 pF TYP.
- High G<sub>ps</sub> : 23 dB TYP.
- Low NF : 1.3 dB TYP.

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25 \ ^{\circ}C$ )

Drain to Source Voltage	Vdsx	20	V
Gate1 to Source Voltage	Vg1s	±8	V
Gate2 to Source Voltage	Vg2s	±8	V
Drain Current	lр	25	mA
Total Power Dissipation	P⊤	200	mW
Channel Temperature	Tch	125	°C
Storage Temperature	Tstg	-55 to +125	°C



#### ELECTRICAL CHARACTERISTICS (TA = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source Breakdown Voltage	BV <sub>DSX</sub>	20			V	$V_{G1S} = V_{G2S} = -2 V$ , $I_D = 10 \mu A$
Drain Current	IDSS	7	10	25	mA	Vds = 6 V, Vg2s = 3 V, Vg1s = 0
Gate1 to Source Cutoff Voltage	VG1S(OFF)			-2.0	V	$V_{DS} = 8 V, V_{G2S} = 0, I_D = 5 \mu A$
Gate2 to Source Cutoff Voltage	Vg2S(OFF)			-1.5	V	$V_{DS} = 8 V V_{G1S} = 0, I_D = 5 \mu A$
Gate1 Reverse Current	IG1SS			±20	nA	$V_{DS} = 0, V_{G1S} = \pm 8 V, V_{G2S} = 0$
Gate2 Reverse Current	IG2SS			±20	nA	$V_{DS} = 0, V_{G2S} = \pm 8 V, V_{G1S} = 0$
Forward Transfer Admittance	<b>y</b> fs	22	28		mS	$V_{DS} = 6 V, V_{G2S} = 3 V, I_D = 10 mA$
						f = 1 kHz
Input Capacitance	Ciss	4.0	5.0	6.5	pF	$V_{DS} = 6 V$ , $V_{G2S} = 3 V$ , $I_D = 10 mA$
Output Capacitance	Coss	2.2	2.9	3.7	pF	f = 1 MHz
Reverse Transfer Capacitance	Crss		0.05	0.08	pF	
Power Gain	Cps	21	24		dB	$V_{DS} = 10 \text{ V}, V_{G2S} = 5 \text{ V}, I_D = 10 \text{ mA}$
Noise Figure	NF		1.2	2.5	dB	f = 200 MHz

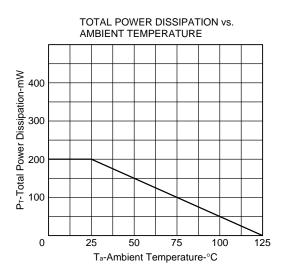
Ibss classification V11 7-13 mA V12 11-19 mA V13 17-25 mA

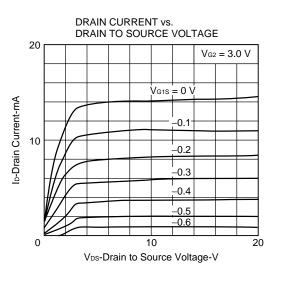
20

10

Ip-Drain Current-mA

#### TYPICAL CHARACTERISTICS (TA = 25 °C)





FORWARD TRANSFER ADMITTANCE vs. GATE1 TO SOURCE VOLTAGE

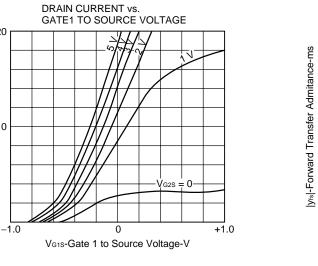
Vg2s

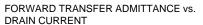
3 \

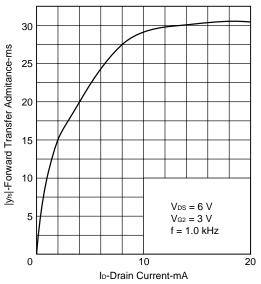
Vos = 6 V

40

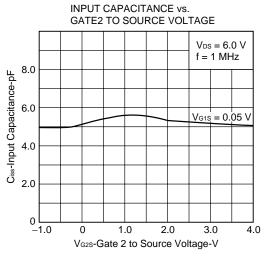
30

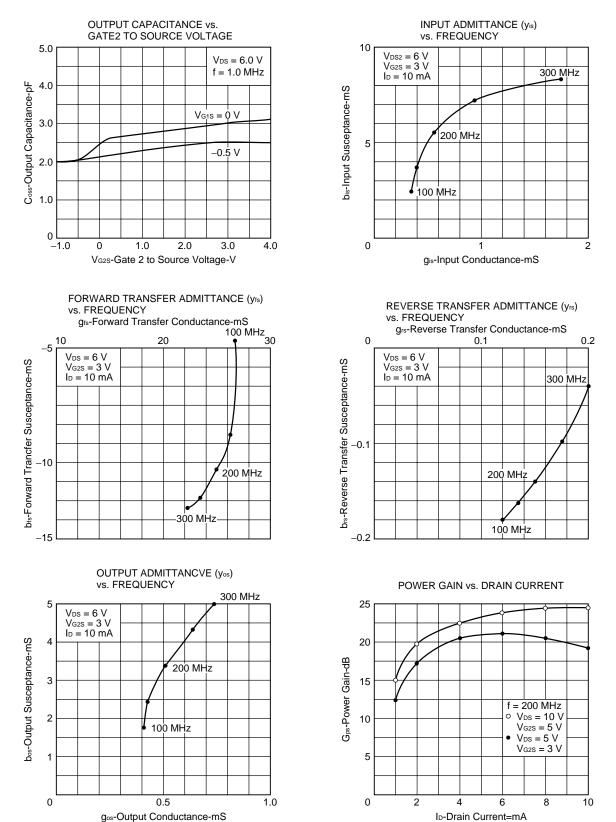




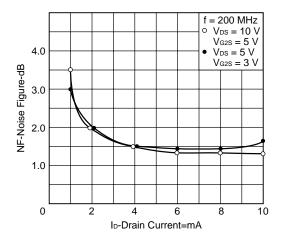


20 10 0 V ء 0 -1.0 0 1.0 VG1S-Gate 1 to Source Voltage-V

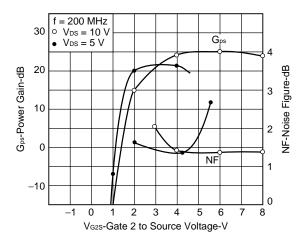




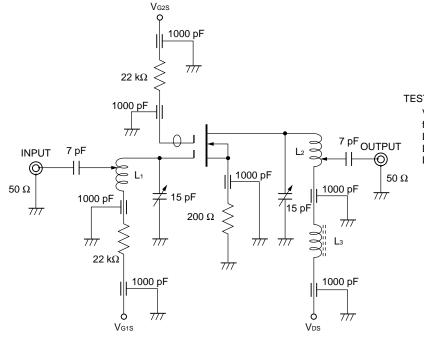
NOISE FIGURE vs. DRAIN CURRENT



NOISE FIGURE, POWER GAIN vs. GATE2 TO SOURCE VOLTAGE



**TEST CIRCUIT** 



TEST CONDITION  $V_{DS} = 10 V, V_{G2S} = 5 V, I_D = 10 mA$  f = 200 MHzL1:  $\phi 0.6 mm U.E.W. 7 mm 3T$ L2:  $\phi 0.6 mm U.E.W. 7 mm 3T$ L3: RFC 2.2  $\mu$ H [MEMO]

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Anti-radioactive design is not implemented in this product.

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